

SHEET INDEX

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SYMBOL
HORIZONTAL DRIVER
ELEMENT IDENT
A

TERM. MOD.	FNCT.	TERM.	LOC.
HS1	I	304	1G4
HS1	I	305	1G4
HS2	I	306	1G4
HS3	I	307	1G4
HS4	I	308	1G4
HS5	I	309	1G4
HS6	I	110	1G4
HS7	I	111	1G4
HS8	I	112	1G4
HS9	I	113	1G4
HS10	I	114	1G4
EDN	B	106	1G5
FVIRT	B	303	1G5
HD	B	204	1G4
HI	B	205	1G4
H2	B	206	1G4
H3	B	207	1G4
H4	B	208	1G4
H5	B	209	1G4
H6	B	011	1G4
H7	B	012	1G4
H8	B	013	1G4
H9	B	014	1G4
HTD	B	015	1G4
+3	P	000	1F4
+3	P	119	1F4
+24	P	218	1F4
+24	P	317	1F4
GRD	G	200	1F4
GRD	G	200	1F4

RECORD OF CHANGES

CHG. ISS.	PREV. FURN.	STD.	MFR DISC.	SEE NOTE

NOTES

- GROUND RETURN
- UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS
CAPACITANCE VALUES ARE IN MICROFARADS
VALUES PRECEDED BY THE SYMBOL "K" ARE IN KILOHMS
VALUES PRECEDED BY THE SYMBOL "M" ARE IN MEGOHMS
- BATTERY AND GROUND TERMINALS FOR THIS CIRCUIT PACK ARE AS FOLLOWS:

FUNCTION	TERMINAL
+3	000, 119
+24	218, 317
GRD	200, 203
- HORIZONTAL MOUNTING CENTERS ARE 0.5 INCH.

SYSTEM USED ON	DESIGN CONTROL
NO. 28 ESS	IN

SUPPORTING INFORMATION

CATEGORY	NO.
CIRCUIT PACK CODE	FC204
CONNECTOR ON FRAME	947A OR 947C
SERIES NO. IDENT	ACCEPTABLE SERIES
	2

CURRENT DRAIN: 10mA +3
55mA +24

SHEET INDEX NOTES

- WHEN CHANGES ARE MADE IN THIS DRAWING ONLY THOSE SHEETS AFFECTED WILL BE REISSUED.
- THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.
- THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE FIRST SHEET.
- SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.
- THE LAST ISSUE NUMBER OF THE FIRST SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.

NOTICE - NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.

1105

FC204 CIRCUIT PACK
HORIZONTAL DRIVER
CIRCUITBELL TELEPHONE LABORATORIES
INCORPORATED

2

CPS-FC204
3 SHEETS

CHG. ISS. ON CHG. SHEET	DATE	ISSUE	APPROV.
1	10-5-53	ALSOBENT	WTS
201	1-24-74	WTS	WTS
301	3-10-74	WTS	WTS
401	4-3-74	WTS	WTS
501	8-17-75	WTS	WTS

ISSUE
5B1AT&T
STANDARD

PART OF CPS FC204

HORIZONTAL DRIVER

COMPONENT LIST

CAPACITOR

DESIG CODE
[17] C1, C2 6005-1.0

DIODE

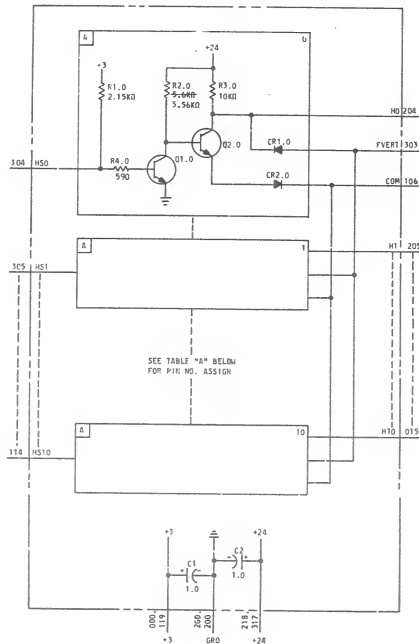
DESIG CODE
[11] CR1, O-CR1, 10 458H
[13] CR2, O-CR2, 10 458H

RESISTOR

DESIG CODE
[11] R1, O-R1, 10 299A-2-55K- 10K5-20616 L1A, 2.15K
[13] R2, O-R2, 10 45-5-55K- 5.55K
[11] R3, O-R3, 10 299A-10K- 10K
[13] R4, O-R4, 10 299A-590- KS-20616 L1B, 590

TRANSISTOR

DESIG CODE
[11] Q1, O-Q1, 10 64W
[13] Q2, O-Q2, 10 64W



SEE TABLE "A" BELOW
FOR PIN NO. ASSIGN

TABLE "A"

GROUP	HS	H
0	30A	204
1	30S	205
2	30S	206
3	30T	207
4	30H	208
5	309	209
6	110	011
7	111	012
8	112	013
9	113	014
10	114	015

FC204 CIRCUIT PACK

BELL TELEPHONE LABORATORIES
INCORPORATED

65

CPS-FC204
SHEET 2

581

PART OF CPS FC 204

HORIZONTAL DRIVER CIRCUIT

CIRCUIT DESCRIPTION

A. FUNCTION

THIS CIRCUIT PACK DRIVES THE HORIZONTAL LEADS OF THE CENTRAL PULSE DISTRIBUTOR (CPD) MATRIX OF THE NO. 20 I/O CONTROL CIRCUIT.

B. DETAILED DESCRIPTION

RESISTORS R1 AND R4 AND TRANSISTOR Q1 ARE CONNECTED IN AN INPUT BUFFER CONFIGURATION SO THAT THE INPUTS (HSD THROUGH HSD10) CAN BE DRIVEN BY 1A LOW-POWER GATES. THE OUTPUT LEADS (HD THROUGH HD10) ARE DESIGNED TO DRIVE A 200 TO 250 OHM LOAD CONNECTED TO 6-PROXIMATELY 15 VOLTS. THE COM LEAD, WHICH IS COMMON TO EACH ONE OF THE 11 CIRCUITS, RETURNS TO THE LEVEL-CONTROL BOARD (FC 207), WHEN A CPD PULSE IS NOT TO BE PRODUCED. EACH ONE OF THE INPUTS (HSD THROUGH HSD10) IS HIGH (INPUT VOLTAGE GREATER THAN 1.5V VOLTS). THIS RESULTS IN TRANSISTOR Q1 BEING SATURATED AND TRANSISTOR Q2 BEING CUT OFF. WHEN A CPD PULSE IS TO BE PRODUCED, ONE OF THE 11 CIRCUITS ON THIS PACK WILL BE SELECTED. A SELECTION IS MADE BY GROUNDING ONE OF THE INPUT LEADS SERVING THE INPUT LEAD LOG, WHICH RESULTS IN A LOW-IMPEDANCE PATH BETWEEN ONE OF THE OUTPUT LEADS AND THE COM LEAD.

MAINTENANCE TESTING

THE FVERT LEAD IS USED FOR MAINTENANCE TESTING. IT CAN SIMULATE THE EFFECT OF GENERATING A CPD PULSE ON THE HORIZONTAL DRIVE CIRCUITS WITHOUT ALLOWING CURRENT THROUGH THE CPD MATRIX. UNDER NORMAL OPERATION (NON-MAINTENANCE MODE) DIODE CR1 PREVENTS THIS LEAD FROM PROVIDING A LOW-IMPEDANCE PATH TO GROUND. SHORT CIRCUITS OR OPEN CIRCUITS IN ANY ONE OF THE TRANSISTORS OR DIODES SHOULD BE DETECTED DURING NORMAL ERROR CHECKING OR PERIODIC MAINTENANCE TESTING.

FILTERING

CAPACITORS C1 AND C2 PROVIDE FILTERING FOR THE +5 AND +24 VOLT POWER SUPPLIES USED ON THIS CIRCUIT PACK.

C. SYMBOL/LEAD NOMENCLATURE

SYMBOL	TYPE	DEFINITION
COM	BU	COMMON-CONNECTS TO THE COM(-) LEAD OF THE LEVEL-CONTROL BOARD AND PROVIDES POWER FOR THE HORIZONTAL MATRIX SELECTION.
FVERT	I	FALSE VERTICAL-PROVIDES A VERTICAL SELECTION PATH FOR MAINTENANCE WHICH DOES NOT ACCESS A CPD TRANSFORMER. THIS ALLOWS TESTING OF HORIZONTAL DRIVERS WITHOUT ALLOWING CURRENT TO FLOW IN THE MATRIX.
HD	Ø	HORIZONTAL DRIVER 0 +
HSD	I	HORIZONTAL SELECT 0 AN
H1	Ø	HORIZONTAL DRIVER 1 +
HSD1	I	HORIZONTAL SELECT 1 AN
H2	Ø	HORIZONTAL DRIVER 2 +
HSD2	I	HORIZONTAL SELECT 2 AN
H3	Ø	HORIZONTAL DRIVER 3 +
HSD3	I	HORIZONTAL SELECT 3 AN
H4	Ø	HORIZONTAL DRIVER 4 +
HSD4	I	HORIZONTAL SELECT 4 AN
H5	Ø	HORIZONTAL DRIVER 5 +
HSD5	I	HORIZONTAL SELECT 5 AN
H6	Ø	HORIZONTAL DRIVER 6 +
HSD6	I	HORIZONTAL SELECT 6 AN
H7	Ø	HORIZONTAL DRIVER 7 +
HSD7	I	HORIZONTAL SELECT 7 AN
H8	Ø	HORIZONTAL DRIVER 8 +
HSD8	I	HORIZONTAL SELECT 8 AN
H9	Ø	HORIZONTAL DRIVER 9 +
HSD9	I	HORIZONTAL SELECT 9 AN
H10	Ø	HORIZONTAL DRIVER 10 +
HSD10	I	HORIZONTAL SELECT 10 AN

K BU - BUS

I - INPUT

Ø - OUTPUT

* THE SELECTED DRIVER OUTPUT IS CONNECTED THROUGH A LOW IMPEDANCE TO THE COM LEAD. THE UNSELECTED LEADS WILL BE HELD HIGH BY A HIGH-IMPEDANCE PATH TO +24 VOLTS.
 AN SELECTED WHEN LOW

CPS-FC 204

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FC204 CIRCUIT PACK
HORIZONTAL DRIVER CIRCUIT

BELL TELEPHONE LABORATORIES

CPS-FC 204
SHEET 3